

2812
#7
Election
11/2/03
Mullish

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Masamoto TAGO

Appln. No.: 09/998,243

Confirmation No.: 6291

Filed: December 03, 2001

Docket No: Q67536

Group Art Unit: 2812

Examiner: Lourdes C. Cruz

For: COMPACTY SEMICONDUCTOR DEVICE CAPABLE OF MOUNTING A
PLURALITY OF SEMICONDUCTOR CHIPS WITH HIGH DENSITY AND METHOD
OF MANUFACTURING THE SAME

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

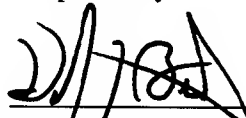
Sir:

In response to the restriction requirement, dated November 29, 2002, Applicant elects
Group I, claims 1-18 for examination. This election is made without traverse.

Applicant reserves the right to file a Divisional Application directed to non-elected
claims 19-25.

The USPTO is directed and authorized to charge all required fees, except for the Issue
Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any
overpayments to said Deposit Account.

Respectfully submitted,



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WASHINGTON OFFICE



23373

PATENT TRADEMARK OFFICE

Date: December 30, 2002